



founded in
2003

KAL - Large IP Cores:

VIP cores:

- PCIe, USB, AMBA
- SD/SDIO, eMMC
- UniPro, I3C, Soundwire
- DDR 2/3/4, LPDDR, ONFI, SATA
- HDMI
- CAN, CANFD, FLexRay
- SPI, I2C, SMBus

Military/Aerospace

- 1553

Multimedia

- H.264
- JPEG

Automotive:

- CAN/CAN-FD
- FlexCAN
- FlexRay
- Lin

AHB/APB Peripherals:

- AHB/AXI/APB bridges
- Inter Connect complete IP

Memory Controllers:

- SD/SDIO Controller
- SDRAM Controller
- DDR / SDRAM Controller
- NAND Flash Controller
- Flash/EEPROM/SRAM Controller
- PCMCIA/CompactFlash Host Adapter
- PCMCIA/CompactFlash Slave Controller

CPU Cores:

END-TO-END IC LAYOUT SERVICES BY ICMASK

Fully managed outsourced solution

For companies without their own internal IC layout teams or for peak times, we offer a full end-to-end service.

- Full service from post design to tapeout
- Time and materials or fixed priced contracts
- Easier project management
- Consistent quality
- Latest technology nodes
- Flexible service

Analog layout/RF Layout

- Input -> Schematic design, Output -> GDSII (or EDA related database)
- "Silicon Aware" with focus on matching, parasitic performance and design for manufacturability
- Expertise in CMOS, RFCMOS, BiCMOS, SiGe & SOI processes
- Deep understanding of layout effects:
 1. Parasitics
 2. Antenna effects
 3. Dummy fill
 4. Isolation issues,
 5. Shielding
 6. Shallow trench isolation
 7. Signal integrity
- Block / toplevel level layout
- RF Layout of radio transceivers with circuits operating up to 40GHz for applications such as
 1. GSM
 2. GPRS
 3. 3G
 4. WiFi
 5. WiMax
 6. UWB
- Timing driven layout

Digital Layout

- Physical synthesis
- Floorplanning
- Place and Route
- Physical verification
- Timing Closure

- 32 bit – ColdFire V4
- 8 bit - 8051
- 8 bit- HC68HC11
- 8 bit - PIC Processor
- 8 bit – Z80
- 16 bit – D6800

- GDSII

End-to-end service including licenses

- As above, with licenses for the customer's preferred EDA platform provided by IC Mask Design

KAL is representing ICMASK in Israel.

Clock Synchronization:

- IEEE 1588 Slave
- IEEE 1588 Master
- IEEE 1588Master/Salve
- IEEE 1588 PTP Stack
- IEEE 1588 L2/L3 Solution

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Peripherals:

- HDLC/SDLC
- Smart Card Reader Unit
- EEPROM SPI Ctrl
- LCD Ctrl
- Floating Point Unit
- I2C/I3C Master/Slave
- SPI Master/Slave
- CAN/CAN FD bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller 10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Salve
- On Chip Bus Analyzer

We are looking forward to hear from you.
Contact us for more information.

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Until the next eNews,

Thanks you for your attention.

KAL

PCI Bus Controllers and Peripherals:

- PCI Express
- PCI-X Host Bridge Master/Target
- PCI Host Bridge Master/Target
- PCI-PCI Bridge

- **PCI-ISA Bridge**
- **PCI Bus Arbiter**

Modulation:

- **ADPSM**

Analog IP Cores:

- **Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.**
- **We are expert in custom analog IP**

[Contact us for data sheet](#)