

KAL - Large IP Cores:

VIP cores:

- PCIe, USB, AMBA
- SD/SDIO, eMMC
- UniPro, I3C, Soundwire
- DDR 2/3/4, LPDDR, ONFI, SATA
- HDMI
- CAN, CANFDm, FLexRay
- SPI, I2C, SMBus

Memory Controllers:

- **SD/SDIO Controller**
- **SDRAM Controller**
- **DDR / SDRAM Controller**
- **NAND Flash Controller**
- **Flash/EEPROM/SRAM Controller**
- **PCMCIA/CompactFlash Host Adapter**
- **PCMCIA/CompactFlash Slave Controller**

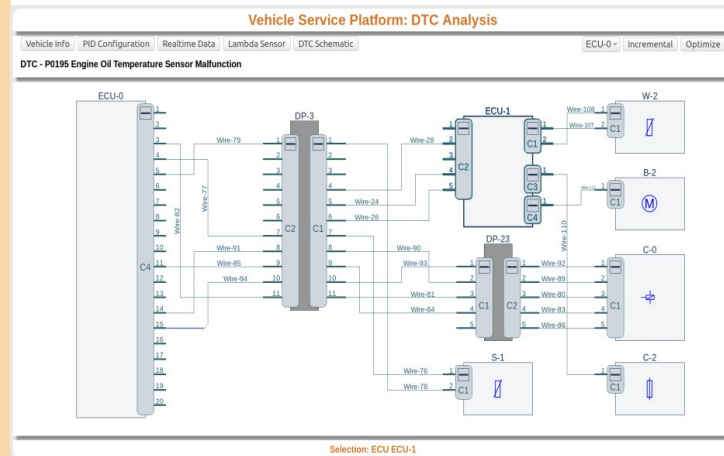
CPU Cores:

- **32 bit - NEW**
- **8 bit - 8051**
- **8 bit- HC68HC11**
- **8 bit - PIC Processor**
- **8 bit – Z80**
- **16 bit – D6800**

Clock Synchronization:

- **IEEE 1588 Slave**
- **IEEE 1588 Master**
- **IEEE 1588Master/Slave**
- **IEEE 1588 PTP Stack**
- **IEEE 1588 L2/L3**

E-engine™: Automatic, Scenario-Specific Schematic Generation for Smart Vehicle Services and complex systems



The complexity of modern automotive electronic systems has exploded, creating a major problem for vehicle service operations. Technicians and mechanics must wade through hundreds of pages of incomprehensible, sometimes out-of-date, documentation, attempting to apply complex schematics to unfamiliar repair scenarios. This is a significant challenge for automotive and aerospace manufacturers where the effective maintenance of their products is a key differentiator. The initial documentation production, applying document revisions, and providing the flexibility to handle vehicle variations all require a considerable investment.

Concept Engineering's E-engine solves these issues by providing a documentation solution that renders schematics specifically to individual maintenance scenarios. The schematics are generated from a database that is converted directly from original CAD drawings, saving authoring effort, eliminating a source of errors, and allowing for easy revisions.

- Easy to Understand: Visual, clear, compact schematics, rendered dynamically for specific repair scenarios for fast issue resolution
- Easy to Use: Smart, interactive, web-based documentation, for easy installation, efficient updates,

Solution

Peripherals:

- HDLC/SDLC
- Smart Card Reader Unit
- EEPROM SPI Ctrl
- LCD Ctrl
- Floating Point Unit
- I2C Master/Slave
- SPI Master/Slave
- CAN bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller 10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Slave
- On Chip Bus Analyzer

PCI Bus Controllers and Peripherals:

- PCI Express
- PCI-X Host Bridge Master/Target
- PCI Host Bridge Master/Target
- PCI-PCI Bridge
- PCI-ISA Bridge
- PCI Bus Arbiter

Modulation:

- ADPSM

AHB/APB Peripherals:

- AHB Bus Master/Slave
- APB Bus Master/Slave
- AHB/AXI DMA Controller
- AXI Bus Master/Slave

Analog IP Cores:

- Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.

and variation handling

- Easy to Produce: Database created directly from CAD design data for fast, error-free, easily-maintained documentation

KAL is representing Concept Engineering GmbH in Israel.

www.KALtech.co.il

We are looking forward to hear from you.
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Until the next eNews,

Thanks you for your attention.

KAL

- We are expert in custom analog IP

[Contact us for data sheet](#)

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