



## KAL - Large IP Cores:

### Memory Controllers:

- **SD/SDIO 2.0/3.0 Controller**
- SDRAM Controller
- **DDR/DDR2/DDR3 SDRAM Controller**
- NAND Flash Controller
- Flash/EEPROM/SRAM Controller
- PCMCIA/CompactFlash Host Adapter
- PCMCIA/CompactFlash Slave Controller

### CPU Cores:

- **32 bit - NEW**
- **8 bit - 8051**
- 8 bit- HC68HC11
- 8 bit - PIC Processor
- 8 bit – Z80
- 16 bit – D6800

### Clock Synchronization:

- IEEE 1588 Slave
- IEEE 1588 Master
- IEEE 1588 Master/Slave
- IEEE 1588 PTP Stack
- IEEE 1588 L2/L3 Solution

### Peripherals:

- HDLC/SDLC
- Smart Card Reader Unit
- EEPROM SPI Ctrl
- LCD Ctrl
- Floating Point Unit
- I2C Master/Slave
- SPI Master/Slave

Today we'd like to focus and present to you our partner Eureka Technology from the US. **We have already many customers form big chip makers and small startups while all sold IPs are gold in silicon!**

## Successful Memory Controller for ASIC/FPGA by Eureka Technology Inc - USA

Eureka Technology Inc. provides reusable IP cores for ASIC, FPGA and System on Chip designs. These system level function cores are designed to:

**Shorten Time-to-market  
Eliminate Design Risks  
Reduce Development Costs**

As today's technologies evolve in a very fast pace, design engineers are constantly looking for ways to speed up the design cycle in order to have product in the market ahead of the competitions. The use of reusable IP cores in ASIC, FGPA and system on chip design has emerged as the methodology of choice to address the needs for rapid productization, fast prototyping and software/hardware co-development.

Eureka Technology help design engineers stay in the forefront of this new design methodology by providing reusable IP cores. Our reusable IP cores are silicon proven and pre-verified to meet and exceed customer requirements. Design risk is virtually eliminated since each one of these cores has been fully tested and proven in real world applications.

Since founded in 1993, Eureka Technology has established itself as a leading reusable IP cores provider with customer base in the United States, Japan, and Europe. We have

- CAN bus
- LIN bus
- Programmable Peripheral Interface
- UART, UART with FIFO
- PWM
- Timer 8254
- Programmable Timer
- Interrupt Controller
- Ethernet Controller 10/100/1000 BaseT
- DMA Controller
- USB 1.0/2.0 Host/Slave
- On Chip Bus Analyzer

provided reusable IP cores to many market leaders in the computer, electronics and semiconductor industries. Our technologies have been incorporated into tens of million dollars' worth of products sold by our customers. We also have entered partnership agreements with leading silicon vendors to incorporate our reusable IP cores into their silicon products. However, our important partnership is the one with our customers and we would like to be the design partner for your next project.

PCI Bus Controllers and Peripherals:

- PCI Express
- PCI-X Host Bridge Master/Target
- PCI Host Bridge Master/Target
- PCI-PCI Bridge
- PCI-ISA Bridge
- PCI Bus Arbiter

**Eureka memory controllers includes: NOR and NAND Flash controllers, SDRAM controllers, DDR 2/3 controllers. We customize the CPU interface for single port or multi port with many types of bridges such as AHB/AXI and others a long with custom arbitration and DMAs.**

**KAL is representing Eureka in Israel.**

[www.KALtech.co.il](http://www.KALtech.co.il)

Modulation:

- ADPSM

We are looking forward to hear from you.

AHB/APB Peripherals:

- AHB Bus Master/Slave
- APB Bus Master/Slave
- AHB/AXI DMA Controller
- AXI Bus Master/Slave

Contact us for more information.

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<http://www.KALtech.co.il>

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Analog IP Cores:

- Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.
- We are expert in custom analog IP

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Contact us for data sheet

Until the next eNews,

Thanks you for your attention.

KAL

