eNewsletter

KAL - Large IP Cores:

Analog IP Cores:

- **DDR2/3 PHY**
- **Analog IP cores (ADC, DAC, PLL,) are available – Please contact us.**
- **We are expert in custom analog IP**

CPU Cores:

- **8 bit - 8051**
- **8 bit- HC68HC11**
- **8 bit - PIC Processor**
- **8 bit – Z80**
- **16 bit – D6800**
- **DSP – MSP430**
- **32 bit - ARM 9xx/11xx**

Memory Controllers:

- **SD/SDIO 2.0/3.0 Controller**
- **SDRAM Controller**
- **DDR/DDR2/DDR3 SDRAM Controller**
- **NAND Flash Controller**
- **Flash/EEPROM/SRAM Controller**
- **PCMCIA/CompactFlash Host Adapter**
- **PCMCIA/CompactFlash Slave Controller**

Clock Synchronization:

- **IEEE 1588 Slave**
- **IEEE 1588 Master**
- **IEEE 1588 Master/Salve**

At this eNews we would like to introduce you, Eureka Technology's

Development Board for SD or MMC

Eureka Technology Inc., a leading provider of high performance IP cores, Provide a SD Host Development Platform that allows SD card developers to quickly design and validate SD and MMC systems at lower cost.

This development platform is the newest member of Eureka's SD family of products which also includes the SD host and SD slave IP cores. These IP cores are available with many optional features to allow customer to develop differentiating products

Key Features (SD IP Cores)

- Compatible with SD/SDIO specification 2.0/3/0
- Supports Multimedia Card (MMC) with 1, 4 and 8-bit transfers
- SD Host and SD Slave (card) functions
- Supports SD memory, SDIO, SD combo and MMC
- Standard and high capacity (SDHC) support
- Built-in DMA and interrupt functions
- High speed data transfer with internal data buffer
- Many optional features to create product differentiation
- Silicon proven in many products in production
- Optimized for ASIC and FPGA implementation

The SD host development system's SD socket that can accept any standard SD or MMC cards. The FPGA is pre-programmed with the SD host IP core and comes with the IP core bit-map license. The SD host development board includes on board FLASH device and expansion header.

About Eureka Technology Inc:

- **IEEE 1588 PTP Stack**
- **IEEE 1588 L2/L3 Solution**

Peripherals:

- **Floating Point Unit**
- **I2C Master/Slave**
- **SPI Master/Slave**
- **CAN bus**
- **LIN bus**
- **Programmable Peripheral Interface**
- **UART, UART with FIFO**
- **PWM**
- **Timer 8254**
- **Programmable Timer**
- **Interrupt Controller**
- **Ethernet Controller 10/100/1000 BaseT**
- **DMA Controller**
- **USB 1.0/2.0 Host/Salve**
- **On Chip Bus Analyzer**

PCI Bus Controllers and Peripherals:

- **PCI Express**
- **PCI-X Host Bridge Master/Target**
- **PCI Host Bridge Master/Target**
- **PCI-PCI Bridge**
- **PCI-ISA Bridge**
- **PCI Bus Arbiter**

Encryption:

- **AES 128bit/256bit**
- **ECC**

AHB/APB Peripherals:

- **AHB Bus Master/Slave**
- **APB Bus Master/Slave**

Eureka Technology Inc. is a leading intellectual property (IP) provider for ASIC, FPGA and system designers. The company specializes in the integration and customization of standard IP core to meet customer requirements.

Eureka offers a wide range of silicon proven system core logic and peripheral function cores for different CPU and bus standards including PowerPC™, AHB™, AXI™, PCI™, PCI-X™, PCI Express™, Cardbus™, SDR/DDR SDRAM, NAND Flash, Secure Digital (SD™), MMC, CompactFlash™ and PCMCIA™.

These IP cores are designed to improve the design time-to-market, eliminate design risks, and reduce development costs for System-on-chip (SoC) designs. Located in Silicon Valley, California, Eureka Technology has pioneered the use of IP cores as a standard methodology in IC design and has licensed hundreds of IP cores to many leading companies in the semiconductor and electronic industries.

With customer base in Israel, US, Europe, Japan and other parts of Asia, the company has built many long term business relationships with its customers after their initial successes.

Contact us for more information.

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Untill the next eNews,

Thanks yu for your attenstion.

KAL

- **AHB/AXI DMA Controller**
- **AXI Bus Master/Slave**

MIPS CPU Interface:

- **MIPS - SysAD Bus Slave**
- **MIPS - SysAD Bus to PCI Host bridge**
- **MIPS - EC interface to SDRAM Controller**
- **MIPS - EC Interface to PCI Host Bridge**
- **MIPS - EC Interface Bus Slave**

PowerPC CPU Interface:

- **Power PC Bus Master**
- **PowerPC to PCI Host bridge**
- **PowerPC Bus Arbiter**
- **PowerPC Bus Slave**

ARC CPU Interface:

- **ARC - Peripheral Controller for ARCtangent**
- **ARC – ARCtangent to PCI host Bridge**

Contact us for data sheet

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