

IP Cores Offering by KAL's Partners:

As we established the company in 2003 we recognized that IP cores are mandatory for fast and low risk ASIC/FPGA project, therefore we partnered with professional IP vendors with proven support record. Our vendors offering leading IPs for SOC (System-On-Chip), high-speed performance, low power demand and small foot print.

IO Libs:

- TSMC: 12,16,22,28,40,45,55,65,110,130,180nm
- GF: 12,28,55,65,180nm
- Samsung: 11,28nm

Special IOs:

- I2C/DDC/CEC, RMII, eMMc, Omni, SPI, LVCOMS, LVTTTL, HSTL, SSTL, GPIO, ODIO, LVDS, ESD, RF ESD, HV ESD
- Custom IOs/ESD development

CPU Cores:

- RISK-V
- Royalty-free 32-bit CPU
- 8bit – 8051 (Pipelined/high performance)
- PIC, HC11, 6800

Automotive:

- CAN-Full (ISO 11898-1:2015, ISO26262, AUTOSAR and SAE J1939 support)
- CANFD
- LIN

Encryption/Crypto:

- AES
- Hash and HMAC FuncDons Accelerator
- ECDSA sign engine
- ECC
- EncrypDon Standard Cryptographic Co-Processor
- SHA2-256

Peripherals:

- Ethernet MAC/TSN 1/10/25/100G
- Interlaken
- JESD
- CPRI
- Ethernet Security
- IEEE1914.3
- I2C/I3C
- Floating Point Unit
- JTAG (TAP)
- USB
- LCD CTRL
- HDLC/SDLC
- Smart Card Reader
- EEPROM SPI CTRL
- SPI (Dual/Quad)
- UART
- Timer/WDT
- Int CTRL
- GPIO
- Programable Peripherals i/f
- PWM
- IrDA CTRL
- MIL 1533 bus
- DMA

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Sync

- 1588 Master/Salve + PTP Stack
- 1588+Gbit Eth

AMBA:

- AMBA Bus Fabric / Bridges (AXI/AHB/APB)
- DMA

Code Modulation:

- ADPCM

Video/Image:

- H.264 Encoder
- H.264 Code
- JPEG Encoder/ Decoder

Analog IPs:

	GF 22nm	TSMC 40nm	TSMC 28nm
ADC 320SPS 11bit	V		V
DAC 500SPC 12bit	V		V
PLL 24Ghz	V		
28Ghz RF front end	V		
77Ghz RF front end	V		
Bios/Ban Gap	V	V	V
Analog Filters	V	V	V
LTE-NBIoT/CatM	V	V	V
3GPP Compliant RF Transceiver			
Wifi Ax 2.4GHz/5GHz/6e RF Transceiver	V	V	V
GNSS L1/L5 RF Receiver	V	V	V
802.11aH RF Transceiver	V	V	v

We are specialized in high precision analog IP per the customer spec